First-principles calculations of electron states of a silicon nanowire with 100,000 atoms on the K computer

Paper Discussion by Xiangyu Gao
Real Space DFT (RSDFT)

- Kohn-Sham Equation

\[ H_{KS}[\rho] \phi_i(\mathbf{r}) = \varepsilon_i \phi_i(\mathbf{r}) \]

\[ \rho(\mathbf{r}) = \sum_{i=1}^{N} |\phi_i(\mathbf{r})|^2 \]
Real Space DFT (RSDFT)

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Discretized three dimensionally
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Solved numerically
(Sparse matrix)
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FFT
Real Space DFT (RSDFT)

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Better for massively parallel computer
Flow Chart

• Read initial atomic configuration (charge density, Hamiltonian)
• Initial electric orbitals are generated randomly.
• Optimize orbitals with Conjugate-Gradient (CG).
• Calculate charge density and Hamiltonian.
• Iterate this process until Hamiltonian and electric orbitals/wave functions are consistent with each other.
Code and K Computer

- RSDFT is developed by

University of Tsukuba

- Paper published in SC 11.
- Won Gordon Bell Prize.
Code and K Computer

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• 10 peta-flops sustained performance for the LINPACK benchmark (dense matrix calculation).
• LINPACK was introduced by Jack Dongarra

• CPU: SPARC64 VIIIfx.
• Interconnection: Tofu(six dimensional)
• SIMD+OpenMP+OpenMPI
SPARC64 VIII fx

- Produced by

Fujitsu Ltd.
富士通株式会社
SPARC64 VIIIfx

• Produced by

Fujitsu Ltd.
富士通株式会社

• 8 cores

• Shared L2 cache (virtual single processor)

• Hardware barrier/communication (much cheaper than software barrier/communication)

• SIMD: 2 operations per instruction

• Faster instructions for sine and cosine

• Sector cache mechanism
  • Splits cache into two sectors
  • One of them is used to register frequently used data
Parallelization in Orbitals

- Number of MPI tasks are limited to the number of grid points.
- Hard to fully utilize the full K computer
- Parallelization done in orbitals
- Global collectives done only within each group of orbitals, which are closer and cheaper.
Parallelization in Orbitals

Figure 7. Comparison of execution time of GS, MatE/SD, RotV/SD and CG between spatial parallelization only and combination of spatial and orbital parallelization.
Implementation of Gram-Schmidt

\[
\begin{align*}
\overline{\varphi}_1 &= \varphi_1 \\
\overline{\varphi}_2 &= \varphi_2 - \varphi_1 (\varphi_1 \cdot \varphi_2) \\
\overline{\varphi}_3 &= \varphi_3 - \varphi_1 (\varphi_1 \cdot \varphi_3) - \varphi_2 (\varphi_2 \cdot \varphi_3) \\
\overline{\varphi}_4 &= \varphi_4 - \varphi_1 (\varphi_1 \cdot \varphi_4) - \varphi_2 (\varphi_2 \cdot \varphi_4) - \varphi_3 (\varphi_3 \cdot \varphi_4) \\
\overline{\varphi}_5 &= \varphi_5 - \varphi_1 (\varphi_1 \cdot \varphi_5) - \varphi_2 (\varphi_2 \cdot \varphi_5) - \varphi_3 (\varphi_3 \cdot \varphi_5) - \varphi_4 (\varphi_4 \cdot \varphi_5) \\
\overline{\varphi}_6 &= \varphi_6 - \varphi_1 (\varphi_1 \cdot \varphi_6) - \varphi_2 (\varphi_2 \cdot \varphi_6) - \varphi_3 (\varphi_3 \cdot \varphi_6) - \varphi_4 (\varphi_4 \cdot \varphi_6) - \varphi_5 (\varphi_5 \cdot \varphi_6) \\
\overline{\varphi}_7 &= \varphi_7 - \varphi_1 (\varphi_1 \cdot \varphi_7) - \varphi_2 (\varphi_2 \cdot \varphi_7) - \varphi_3 (\varphi_3 \cdot \varphi_7) - \varphi_4 (\varphi_4 \cdot \varphi_7) - \varphi_5 (\varphi_5 \cdot \varphi_7) - \varphi_6 (\varphi_6 \cdot \varphi_7) \\
\overline{\varphi}_8 &= \varphi_8 - \varphi_1 (\varphi_1 \cdot \varphi_8) - \varphi_2 (\varphi_2 \cdot \varphi_8) - \varphi_3 (\varphi_3 \cdot \varphi_8) - \varphi_4 (\varphi_4 \cdot \varphi_8) - \varphi_5 (\varphi_5 \cdot \varphi_8) - \varphi_6 (\varphi_6 \cdot \varphi_8) - \varphi_7 (\varphi_7 \cdot \varphi_8)
\end{align*}
\]

Figure 2. Example of partitioning the ortho-normalization process of orbitals. A triangular area in the first column partition is calculated first, and square areas in the same column partition below the triangular area are calculated concurrently, and so on.

Imbalanced load

Idle processes
Implementation of Gram-Schmidt

(a) Rank No. of Orbital parallel

(b) Imbalanced load    Idle processes

Time

- Triangular area
- Square area
- Wait
- MPI_Bcast
Implementation of Subspace Diagonalization

Figure 4. Balanced mapping of a Hermitian matrix. Block square matrices are evenly distributed to parallel tasks.
## Overall Performance

<table>
<thead>
<tr>
<th>Procedure block</th>
<th>Execution time (sec.)</th>
<th>Computation time (sec.)</th>
<th>Communication time (sec.)</th>
<th>Performance (PFLOPS/%)</th>
</tr>
</thead>
<tbody>
<tr>
<td></td>
<td></td>
<td></td>
<td>Adjacent /space</td>
<td>Global /space</td>
</tr>
<tr>
<td>SCF</td>
<td>5,456.21</td>
<td>4,417.152</td>
<td>83.18</td>
<td>899.05</td>
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<tr>
<td>SD</td>
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<td>3,218.728</td>
<td>27.70</td>
<td>458.87</td>
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<tr>
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<td>337.85</td>
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<tr>
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<tr>
<td>RotV</td>
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<td>1,177.15</td>
<td>-</td>
<td>121.01</td>
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<tr>
<td>CG</td>
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<td>57.66</td>
<td>55.48</td>
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Quite good
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DGEMM in BLAS

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